**Vishay Sfernice** 

LTO 50



#### **FEATURES**

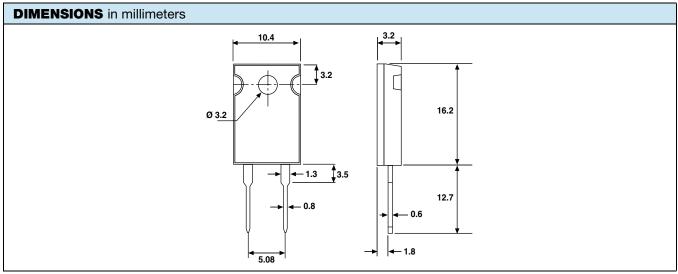
- 50 W at 25 °C case temperature heatsink mounted
- RoHS COMPLIANT



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LTO series are the extension of RTO types. We used the direct ceramic mounting design (no metal tab) of our RCH power resistors applied to semiconductor packages.

- Direct mounting ceramic on heatsink
- Broad resistance range: 0.010  $\Omega$  to 550 k $\Omega$
- Non inductive
- TO-220 package: Compact and easy to mount
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



Note

• Tolerances unless stated: ± 0.3 mm.

| STANDARD ELECTRICAL SPECIFICATIONS |        |                          |  |   |                  |  |   |
|------------------------------------|--------|--------------------------|--|---|------------------|--|---|
| MODEL                              | SIZE   | RESISTANCE<br>RANGE<br>Ω | RATED POWER<br>P <sub>25 °C</sub><br>W | LIMITING ELEMENT<br>VOLTAGE U <sub>L</sub><br>V | TOLERANCE<br>± % | TEMPERATURE<br>COEFFICIENT<br>± ppm/°C | $\begin{array}{c} CRITICAL\\ RESISTANCE\\ \Omega \end{array}$ |
| LTO 50                             | TO-220 | 0.010 to 550K            | 50                                     | 500   | 1, 2, 5, 10      | 150, 250, 700, 900                     | 5K  |

| MECHANICAL SPECIFICATIONS |               |  |  |
|---------------------------|---------------|--|--|
| Mechanical Protection     | Molded        |  |  |
| Resistive Element         | Thick film    |  |  |
| Substrate                 | Alumina       |  |  |
| Connections               | Tinned copper |  |  |
| Weight                    | 2 g max.      |  |  |
| Mounting Torque           | 1 Nm          |  |  |

| ENVIRONMENTAL SPECIFICATIONS |  |  |  |
|------------------------------|--|--|--|
| Temperature Range            | -55 °C to +150 °C  |  |  |
| Climatic Category            | 55 / 155 / 56  |  |  |
| Flammability                 | IEC 60695-11-5<br>2 applications 30 s<br>separated by 60 s |  |  |

| TECHNICAL SPECIFICATIONS  |  |  |  |
|---|--|--|--|
| Onto a heatsink   |  |  |  |
| 50 W at +25 °C (case temp.)<br>R <sub>TH (j - c</sub> ): 2.5 °C/W<br>Free air:<br>2.5 W at +25 °C |  |  |  |
| See Performance table   |  |  |  |
| ± 150 ppm/°C  |  |  |  |
| 1500 V <sub>RMS</sub> - 1 min<br>10 mA max.   |  |  |  |
| $\geq 10^4 \ M\Omega$   |  |  |  |
| ≤ 0.1 μH  |  |  |  |
|   |  |  |  |

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1 For technical questions, contact: sferfixedresistors@vishay.com Document Number: 50050

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## **Vishay Sfernice**

| PERFORMANCE              |   |                     |  |  |
|--------------------------|---|---------------------|--|--|
| TESTS                    | CONDITIONS  | REQUIREMENTS        |  |  |
| Momentary Overload       | EN 60115-1<br>1.5 Pr / 5 s<br>U <sub>S</sub> < 1.5 U <sub>L</sub>     | ± (0.5 % + 0.005 Ω) |  |  |
| Rapid Temperature Change | EN 60115-1<br>IEC 60068-2-14 Test Na<br>5 cycles<br>-55 °C to +155 °C | ± (0.5 % + 0.005 Ω) |  |  |
| Load Life                | EN 60115-1<br>1000 h Pr at +25 °C                                     | ± (1 % + 0.005 Ω)   |  |  |
| Humidity (Steady State)  | MIL-STD-202<br>method 103 B cond. D                                   | ± (0.5 % + 0.005 Ω) |  |  |
| Vibration                | MIL-STD-202<br>method 204 cond. D                                     | ± (0.2 % + 0.005 Ω) |  |  |
| Terminal Strength        | MIL-STD-202<br>method 211 cond. A1                                    | ± (0.2 % + 0.005 Ω) |  |  |
| Shock                    | 100G, MIL-STD-202<br>method 213 cond. I                               | ± (0.5 % + 0.005 Ω) |  |  |

| SPECIAL FEATURES                                      |                 |               |              |              |  |
|---|-----------------|---------------|--------------|--------------|--|
| Resistance Values                                     | ≥ 0.010         | ≥ 0.015 ≥ 0.1 |              | ≥ 0.5        |  |
| Tolerances  | ± 1 % at ± 10 % |               |              |              |  |
| Typical Temperature Coefficient<br>(-55 ° to +155 °C) | ± 900 ppm/°C    | ± 700 ppm/°C  | ± 250 ppm/°C | ± 150 ppm/°C |  |

#### CHOICE OF THE HEATSINK

The user must choose according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 150 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{R_{TH (j - c)} + R_{TH (c - h)} + R_{TH (h - a)}} (1)$$

the fastening device.

P: Expressed in W

 $\Delta T:$  Difference between maximum working temperature and room temperature

 $R_{TH\,(j-c)}$  Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component.

#### Example:

 $R_{TH\ (c\ -a)}$  for LTO 50 power rating 10 W at ambient temperature +25  $^\circ C$ 

Thermal resistance R<sub>TH (j - c)</sub>: 2.5 °C/W

Considering equation <sup>(1)</sup> we have:

$$\begin{split} &\Delta T = 150~^{\circ}C - 25~^{\circ}C = 125~^{\circ}C \\ &R_{TH~(j~-~c)} + R_{TH~(c~-~h)} + R_{TH~(h~-~a)} = \frac{\Delta T}{P} = \frac{125}{10} = 12.5~^{\circ}C/W \\ &R_{TH~(c~-~h)} + R_{TH~(h~-~a)} = 12.5~^{\circ}C/W - 2.5~^{\circ}C/W = 10~^{\circ}C/W \end{split}$$

with a thermal grease  $R_{TH (c - h)} = 1 \text{ °C/W}$ , we need a heatsink with  $R_{TH (h - a)} = 9 \text{ °C/W}$ .

2

R<sub>TH (c - h)</sub>: Thermal resistance value measured between outer side of

the resistor and upper side of the heatsink. This is the thermal resistance of the interface (grease, thermal pad), and the quality of

R<sub>TH (h - a)</sub>: Thermal resistance of the heatsink.

# LTO 50



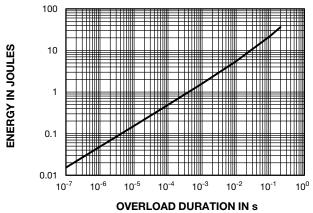
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#### **OVERLOADS**

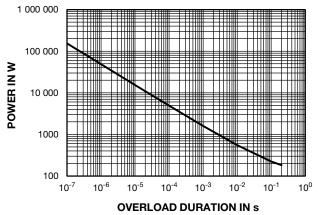
In any case the applied voltage must be lower than the maximum overload voltage of 750 V.

The values indicated on the graph below are applicable to resistors in air or mounted onto a heatsink.

### ENERGY CURVE



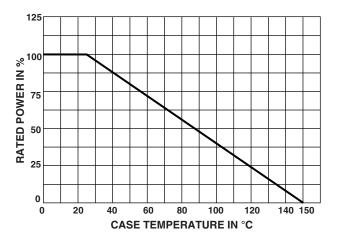
#### **POWER CURVE**



#### POWER RATING

The temperature of the case should be maintained within the limits specified.

To improve the thermal conductivity, surfaces in contact should be coated with a silicone grease and the torque applied on the screw for tightening should be around 1 Nm.



| PACKAGING        |  |
|------------------|--|
| Tube of 50 units |  |

#### MARKING

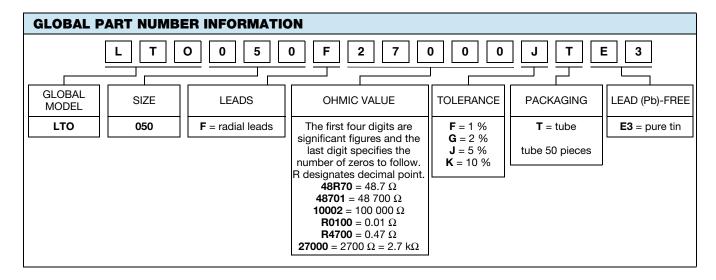
Model, style, resistance value (in  $\Omega$ ), tolerance (in %), manufacturing date, Vishay Sfernice trademark.



LTO 50

## Vishay Sfernice

| ORDERING INFORMATION |       |             |                  |  |  |           |                |
|----------------------|-------|-------------|------------------|--|--|-----------|----------------|
| LTO                  | 50    | F           | 2.7 kΩ           | ±1%  | XXX  | TU50      | e3             |
| MODEL                | STYLE | CONNECTIONS | RESISTANCE VALUE | TOLERANCE<br>± 1 %<br>± 2 %<br>± 5 %<br>± 10 % | CUSTOM DESIGN<br>optional<br>on request:<br>special TCR,<br>shape etc. | PACKAGING | LEAD (Pb)-FREE |



| RELATED DOCUMENTS   |                          |  |  |  |
|---|--------------------------|--|--|--|
| APPLICATION NOTES   |                          |  |  |  |
| Potentiometers and Trimmers                                       | www.vishay.com/doc?51001 |  |  |  |
| Guidelines for Vishay Sfernice Resistive and Inductive Components | www.vishay.com/doc?52029 |  |  |  |



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